Wideband Sigma Delta ADC with VCO-based quantizer

Mots clés :
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Résumé du projet de recherche (Langue 1)

The objective of this thesis is to study, design and implement a wide band, high resolution CT SD ADC with VCO-based multi-bit quantizer. The realization is targeting standard CMOS technology for a low IF receiver suitable for WiMax standard with as low power and low complexity as possible. The designed CT SD ADC performance will be evaluated through comparing its performance to state-of-the-art published solutions. The key parameter in evaluation will be its Figure of Merit which includes the Signal-to-noise-ratio (SNR), the target bandwidth and the total consumed power.